# ELC-PC12NNDR/T ELC-PC12NNAR



# **Instruction Sheet**

Logic Controller with Multi-Function, Multiple Commands

## A WARNING

- This Instruction Sheet only provides descriptions for electrical specifications, general specifications, installation & wiring, troubleshooting and peripherals. Other detail infromation about programming and commands is compatible with ELC-PC/PA/PH series; please see ELC Application Manual. For more information about the optional peripherals, please see individual product manual.
- This is an OPEN TYPE Controller. The ELC should be kept in an enclosure away from airborne dust, humidity, electric shock risk and vibration. Also, it is equipped with protective methods such as some special tools or keys to open the enclosure, so as to avoid the hazard to users and the damage to the ELC.
- Never connect the AC main circuit power supply to any of the input/output terminals, as it will damage the ELC. Check all the wiring prior to power up. To avoid any electromagnetic noise, make sure the ELC is properly grounded  $\bigoplus$ . Do NOT touch terminals when power on.
- Battery replacement: use UL component type: TDRTL-2150/S lithium battery. Refer to following table for battery life. (NOTE: RTC should be reset after changing battery).

Temperature((°C)	0	25	50	70
Life(Years)	9	8	6	5

Precision of calendar timer:

At 0°C/32°F, less than –117 seconds error per month. At 25°C/77°F, less than 52 seconds error per month. At 55°C/131°F, less than –132 seconds error per month.

- Warning Do not disconnect while circuit is live unless area is known to be non-hazardous.
- Power, input and output (I/O) wiring must be in accordance with Class 1, Div. 2 wiring methods Article 501-10(B)(1) of the National Electrical Code.
- Suitable for use in Class 1, Division 2, Groups A, B, C, D or Non-Hazardous locations only.
- Warning Explosion hazard Substitution of components may impair suitability for Class 1, Division 2.
- Warning Wxplosion hazard Do not disconnect equipment unless power has been switched off or the area is known to be Non-Hazardous.

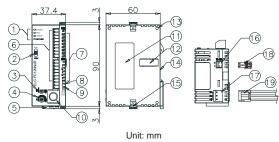
#### INTRODUCTION

#### 1.1 Model Name Explanation and Peripherals

Thank you for choosing Eaton Logic Controller (ELC) series products. The ELC-PC series has a 12-points (8 input points + 4 outputs) ELC main processing unit with multiple commands for use. It also has an 8K Steps program memory to connect to every ELC-PC series expansion unit, including digital I/O (Maximum 128 Inpus / 128 Outputs expansion points), analog module, etc. for various applications.

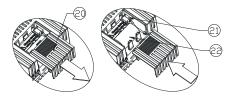
#### 1.2 Product Profile and Outline

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	1	Status indicator: POWER, RUN,
		ERROR, BAT.LOW
	2	RUN/STOP switch
	3	VR0: M1178 Start-up/D1178
	-	Corresponding value
	4	VR1: M1179 Start-up/D1179
		corresponding value
	5	DIN rail clip
1	6	I/O terminals
	7	I/O point indicators
	8	COM1 (RS-232) (Rx) indicator
	9	COM2 (RS-485) (Tx) indicator
	10	COM1 (RS-232) programming port
	11	Nameplate
	12	Expansion port
	13	Mounting hold of the expansion unit

Warning: Battery replacement: Please change the battery within 3 minutes, or the internal data of the ELC (including the program area, perpetual calendar and latched registers) could be lost or destroyed.



14	DIN	rail	(35mm)	)

 14
 DIN fail (3striff)

 15
 Expansion unit clip

 16
 COM2 (RS-485) Communication port

 17
 DC Power input

 18
 2 pin removable terminal (standard accessory)

 19
 Power input cable (standard accessory)

 20
 Battery Cover

20 Battery Cover 21 Battery socket connection 22 Battery mount

2

### FUNCTION SPECIFICATIONS

		lt	ems	Specifications		Remarks				
Cor	ntro	l Metho	b	Stored program, cyclic scan system						
I/O	I/O Processing Method		Method	Batch processing method (when END con is executed)	I/O refresh command is available					
Exe	ecut	ion Spe	ed	Basic commands (several us)	Application Commands (10~hundreds us)					
Pro	gra	m Lang	uage	Commands + Ladder Logic + SFC		Including the Step commands				
Pro	gra	m Capa	city	7,920 STEPS		SRAM + Battery				
Cor	mma	ands		32 Basic sequential commands		168 Application commands				
	Х	Externa	al Input Relay	X0~X177, octal number system, 128 points	Total 256	Correspond to external I/P point				
	Y	Externa	al Output Relay	Y0~Y177, octal number system, 128 points	points	Correspond to external O/P point				
			General	M0~M511, 512 points (*1)						
	м	Auxilia Relay	y Latched	M512~M999, 488 points (*3) M2000~M4095, 2,096 points (*3)	Total 4,096	Contacts can switch to On/Off in program				
		Ttelay	Special	M1000~M1999, 1,000 points (some are latched)	points	in program				
				T0~T199, 200 points (*1)						
			100ms	T192~T199 for Subroutine	Total	When the timer that set by				
(e)	т	Timer		T250~T255, 6 points Accumulative (*4) T200~T239, 40 points (*1)	256	TMR command reaches the preset value, the T contact with				
por			10ms	T240~T245, 6 points Accumulative (*4)	points	the same number will be On.				
Relay (bit mode)			1ms	T246~T249, 4 points Accumulative (*4)						
ay (t			16-bit Count	C0~C95, 96 points (*1)						
Rela			Up 32-bit Count	C96~C199, 104 points (*3) C200~C215, 16 points (*1)	Total	When the counter that set by CNT (DCNT) command				
-	С	Counter	Counter	Counte	Counte	Counter		C216~C234, 19 points (*3)	250	reaches the preset value, the C
						32bit	C235~C245, 1 ph, 1 input, 9 points (*3)	points	contact with the same number will be On.	
			High-speed Count Up/Dn	C246~C250, 1 ph, 2 input, 3 points (*3) C251~C254, 2 ph, 2 input, 3 points (*3)	-	will be On.				
			Initial Step Point	S0~S9, 10 points (*1)						
	S	Char	Zero Point Reset	S10~S19, 10 points (use with IST command) (*1)	Total	Usage device of step ladder diagram (SFC)				
		Step point	General	S20~S511, 492 points (*1)	1,024	Latched Range:				
			Latched	S512~S895, 384 points (*3)	points	Start: D1214 (K512) End: D1215 (K895)				
			Alarm	S896~S1023, 128 points (*3)						
	т	Curren Timer	t Value of the	T0~T255, 256 points	When the timer reaches the preset value, the contact of timer will be On.					
Register (WORD data)	с	Current Value of the Counter		C0~C199, 16-bit counter, 200 points C200~C254, 32-bit counter, 50 points	When the counter reaches the preset value, the contact of counter will be On.					
OR.			General	D0~D199, 200 points (*1)						
Ś			Latched D200~D999, 800 points (*3) Total		Can be memory area for					
ste	D	Data Regist		D2000~D4999, 3,000 points (*3)	5,000	storing data. E and F can be used as the special purpose of				
Reg		regiot	Special D1000~D1999, 1,000 points points		index indication.					
	0		Index	E0~E3, F0~F3, 8 points (*1)						
	None	File Re	gister	0~1,599 (1,600 points) (*4)	Expansion register for storing data.					
	Ν	For Ma Nested	ister Control I Loop	N0~N7, 8 points	Control point of master control nested loop					
	Ρ	For CJ Comm		P0~P255, 256 points	The location point of CJ, CALL.					
Pointer		Interrupt Service	xternal Interrupt	1001 (X0), 1101 (X1), 1201 (X2), 1301 (X3) (X4), 1501 (X5); 6 points (all are rising-ed trigger)		The location pointer of interrupt subroutine				
	Т	r bt	ïme Interrupt	I6 (1ms), I7 (1ms), (=1~99	9ms)					
		+ Tru	li-speed Counter	1010, 1020, 1030, 1040, 1050, 1060; 6 points	3	1				
		Ē	Communication	I150, 1 point	1					
ant	K	I		K-32,768 ~ K32,767 (16-bit operation)		·				
Constant	К	Decim	ai i	K-2,147,483,648 ~ K2,147,483,647 (32-b	it operat	tion)				
Ö	Н	Hexad	ecimal	H0000 ~ HFFFF (16-bit operation), H000		· · · ·				
		mming		COM1: RS-232, COM2: RS-485 (Master/	Slave), (	Can be used at the same time.				
		Volume ar (RTC	e / Perpetual	ELC built-in 2 points VR / ELC built-in RT	С					
			sion Module	Use the same modules of ELC series. (M	ax. 8 Ex	(pansion Unit points)				
- 1- 5		P =				,				

 $^{\ast}\ensuremath{1}\xspace$  the non-latched area is fixed, and can't be changed.

\*2: The non-latched area can be changed to a latched area with parameter setting.

\*3: The latched area can be changed to a non-latched area with parameter setting.

\*4: The latched area is fixed, and can't be changed.

	0.00	enel	1	Latche	یا. ا		On a sint as willing			Latched
М	General		_		÷-		Special auxiliary relay			
Auxiliary	M0~I	M511	_	M512~M999			M1000~M1999		M2000~M4095	
Relay				ched (de			Some are latch	ed and		hed (default)
Relay	Non-latch	ed (fixed)		D1200			can't be char			D1202 (K2,000)
			End:	D1201	\/					01203 (K4,095)
т		ms		10 ms			10ms		ns	100 ms
Timer		T199	-	200~T2			T240~T245		~T249	T250~T255
	Non-latch	ed (fixed)	Non-	latched	· /			-	Latched	· /
	16-	bit count u	0	:	32-bit c	our	nt up/down	32-bit h	igh-spee	d count up/down
	C0~C95	C96~	C199	C200~	-C215		C216~C234	C235	5~C245	C246~C255
С		Latched	(default)			L	atched (default)		Latched	(default)
Counter	Non-latched (fixed)	Start: D12	08 (K96)		Non-latched (fixed)		Start: D1210	S	Start: D1212 (K235)	
	(11/04)	End: D1209 (K199)		( /		En	(K216) d: D1211 (K234)	E	End: D1213 (K255)	
	For general	Latched	Special re	gister	Latched			For general		
	S0~S9	S10~S19	S20~S	S20~S511			S512~S895		S	396~S1023
S						Factory setting is latched		It is fixed to be latched		
Step Relay	It is fi	xed to be n	on-latched			rt: D1214 ( K512 )				
			End: D1215 ( K895 )			)				
	Gen	eral		Latche	d		Special regis	ters	Latched	
	D0~[	D199	D	200~D	999		D1000~D1999		D2	000~D4999
D Register	Non lateb	od (fixed)	Fac	Factory setting is latched.		Some are latched and		Factory setting is latched.		
				Start: D1216 (K200) End: D1217 (K999)			can't be char	ged.	Start: D1218 (K2,000) End: D1219 (K4,999)	
File					KC	)~K	1,599			
Register		Latched (fixed)								

When switching between power On/Off or ELC RUN/STOP modes:

3

Memory Type	POWER Off⇔On	STOP⇒RUN	RUN⇔STOP	Clear all M1031 non-latched area	Clear all M1032 latched area	Factory Setting
			M1033=Off, clear			
Non-latched	Clear	Unchanged	M1033=On, unchanged	Clear	Unchanged	0
Latched		Unchang	ed	Unchanged	Clear	0
Special M, Special D, Index register	Initial value	Un	changed	Unchanged		Initial value
File register	Unchanged					0

#### ELECTRICAL SPECIFICATIONS

Item	ELC-PC12NNDR/T	ELC-PC12NNAR				
Power Supply Voltage	ELC: 24VDC (-15%~+20%) (With DC input reverse polarity protection), Expansion Unit: supplied by the ELC					
Fuse	2A / 250VAC					
Power Consumption	3.5W MAX					
Insulation Resistance	> 5 M $\Omega$ at 500 VDC (Between all inp	outs / outputs and earth)				
Noise Immunity	ESD(IEC 61131-2, IEC 61000-4-2): 8 EFT(IEC 61131-2, IEC 61000-4-4): P Communication I/O: 1KV RS(IEC 61131-2, IEC 61000-4-3): 26	ower Line: 2KV, Digital I/O: 1KV, Analog &				
Grounding	0	ot be smaller than the wire diameter of should be grounded directly to the ground				
Environment	Operation: $0^{\circ}C \sim 55^{\circ}C$ (Temperature), Storage: $-25^{\circ}C \sim 70^{\circ}C$ (Temperature),	50~95% (Humidity), Pollution degree 2; 5~95% (Humidity)				
Agency Approvals	UL508 UL1604, Class1,Div2 Operating ten European community EMC Directive 73/23/EEC	nperature code: T5 89/336/EEC and Low Voltage Directive				
Vibration / Shock Resistance	Standard: IEC61131-2, IEC 68-2-6 (T Ea)	EST Fc) / IEC61131-2 & IEC 68-2-27 (TEST				
Weight (approx.) (g)	158					

DC Input Point Electrical Specification						
Input Type	DC (SINK or SOURCE)					
Input Current	24VDC 5n	24VDC 5mA				
Active Level	Off→On	X0,X1: above18.5VDC On⇒O		X0~X7: below 8VDC		
Active Level		X2~X7: above16.5VDC		XU~X7. DEIOW 8VDC		

Responding Time	Adjustable 0~20ms, Default10ms, selected through D1020)		
AC Input Point Electrical Specification			
Rated Input Voltage 100 to 120 VAC (-15%~+10%)			
Maximum Input Current Less than 20 mA			
Minimum ON Voltage	80 VAC		
Maximum OFF Voltage	30 VAC		

Output Point Electrical Specification						
Output Type	Relay-R		Transistor-T			
Current Specification	1.5A/1 point (5A/COM)	0.3A/1 point @ $40^{\circ}$ C; When the output of Y0 and Y1 is high-speed pulse, Y0 and Y1 = 30mA				
Voltage Specification	Below 250VAC, 30VDC	30VDC				
Maximum Loading	75VA (Inductive)	9W/1 point	When the output of Y0 and Y1 is high-speed pulse, Y0 and Y1 = $0.9W$ (Y0 = $32kHz$ , Y1 = $10kHz$ ), Y0			
Maximum Loading	90 W (Resistive)	9W/Tpoint	can be 50Khz using D registers $(10 = 32 \text{ kHz})$ , $11 = 10 \text{ kHz})$ , $10$			
Responding Time	About 10 ms	Off→On 20us On→Off 30us	Y0 and Y1 are specified points for high-speed pulse			

#### MODEL NAME & I/O CONFIGURATION

	Input / Output					Profile		
Model	_	li li	nput Unit	Output Unit		Reference	I/O Configuration	
	Power	Point	Туре	Point	Туре			
ELC-PC12NNAR		8 100~120VAC		4	Relay		DC S/S AC COM input X0 X1 X1 X2 X2 X3 X4 X4	
ELC-PC12NNDR	24VDC	8	DC Sink or	4	Relay		X2         X2           X3         X3           X4         X4           X5         X5           X6         X6           X7         X7           Y0         Y0           C1         C1           Y1         Y1	
ELC-PC12NNDT	C-PC12NNDT		Source	4	Transistor		C0         C0           Y0         Y0           C1         C1           Y1         Y1           Y2         Y2           Y2         Y3           Y3         Y3           •         •           •         •           •         •	

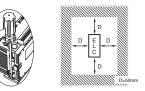
#### **INSTALLATION & WIRING**

#### 5.1 Installation of the DIN rail

The ELC can be secured to a cabinet by using the DIN rail that is 35mm high with a depth of 7.5mm. When mounting the ELC on the DIN rail, be sure to use the end bracket to stop any side-to-side motion of the ELC, thus to reduce the chance of the wires being pulled loose. At the bottom of the ELC is a small retaining clip.

To remove it, pull down the retaining clip and gently pull the ELC away from the DIN rail. As shown on the right:

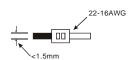
When installing the ELC, make sure that it is installed in an enclosure with sufficient space (as shown on the right) to its surroundings so as to allow heat dissipation.



#### 5.2 Wiring

4

5

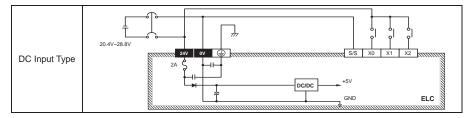


 Please use 22-16AWG (1.5mm) wiring (either single or multiple core) for I/O wiring terminals. The specification for the terminals is as shown on the left. ELC terminal screws should be tightened to 1.95 kg-cm (1.7 lb-in). Use Copper Conductor Only, 60/75 °C.

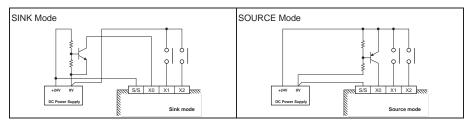
 I/O signal wires or power supply should not run through the same multi-wire cable or conduit.

#### 5.3 Power Input Wiring

- When DC voltage is supplied to the ELC, make sure the power is at terminals 24VDC and 0V (power range is 20.4VDC~28.8VDC). When voltage is lower than 20.4VDC, ELC will stop operating, all outputs will turn OFF and the ERROR LED will flash continuously.
- If the power-cut time is less than 10ms, the ELC still operates unaffectedly. If the power-cut time is too long or the power voltage drops, the ELC will stop operating and all the outputs will be Off.

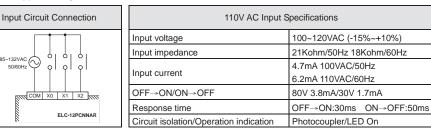


There are two types of DC type wiring: SINK and SOURCE, defined as follows:



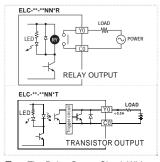
#### 5.5 AC Type Wiring

85~132VAC



#### 5.6 Output Point Wiring

#### Output point wiring

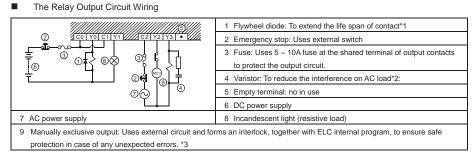


- ELC-PC series have two output modules, relay and transistor. See 1. "Function Specifications" for their specifications.
- 2 Please watch out the connection of common terminals whilewire the outputs. For example, when wiring ELC-PC12NNDR/T,output terminal Y0 uses one common terminal C0, Y1 uses C1, and Y2~Y3 share C2, as shown below



When output points are enabled, their corresponding indicators on the front panel will be on.

Isolation circuit: The optical coupler is used to isolate signals 3. between the circuit inside ELC and input modules.



\*1: For switching direct current on inductive loads, a reverse-current protection diode should be installed in parallel

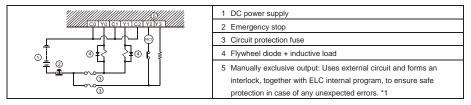
with the load. The relay contact life decreases significantly if this is not done.

- The reverse-current protection diode needs to satisfy the following specifications.
  - The diode is rated for maximum reverse voltage of 5~10 times the load voltage.
  - The forward current is more that the load current

\*2: For switching AC on inductive loads, a surge absorber (0.1uF + "100ohm to 120ohm") should be installed in parallel with the load. The relay contat life decreases significantly if this is not done.

\*3: Ensure all loads are applied to the same side of each ELC output, see above figure. Loads which should NEVER simultaneously operate(e.g. direction control of a motor), because of a critical safety situation, should not rely on the ELC's sequencing alone. Mechanical interlocks MUST be fitted to all critical safety circuits.

The Transistor Output Circuit Wiring



\*1: Transistor outputs use internal zener diode(39V) as protection circuitry. When driving the inductive load with transistor output, a reverse-current protection diode can be installed in parallel with the load if necessary.

The reverse-current protection diode needs to satisfy the following specifications.

- The diode is rated for maximum reverse voltage of 5 to 10 times the load voltage.

- The forward current is more than the load current.

## 6

#### TRIAL RUN

Power Indication

The "POWER" LED at the front of the ELC will be lit (in green) if the power is on.

Low Battery Voltage Indication

There is also a "BAT.LOW" LED at the front of the ELC. When the LED is on, it indicates that the battery voltage is insufficient. Please change the battery (within 3 minutes) as soon as possible; otherwise the user programs and the data in latched area may be lost.

- Preparation
  - Prior to applying power, please verify that the power lines and the input/output wiring are correct. And be advised not to supply AC110V or AC220V into the I/O terminals, or it might short-circuit the wiring and would cause direct damage to the ELC.
  - After using the peripheral devices to write the program into the ELC and that the ERROR LED of the ELC is not on, it means that the program in use is legitimate, and it is now waiting for the user to give the RUN command.
  - 3. Use ELC-HHP to execute the forced On/Off test of the output contact.
- Operation & Test

If the "ERROR" LED of the ELC is not blinking, use RUN/STOP switch or the peripheral devices (ELC-HHP or ELCSoft) to give the RUN command, and the RUN indicator will then be on. If the "RUN" LED is not on, it indicates that there is no program inside the ELC.

ELC-HHP could be utilized to monitor the settings and the registered values of the timer (T), the counter (C) and the data register (D) during operation, and moreover, to force the output contacts to conduct the On/Off action. If the ERROR LED is on (but not blinking), it means that the setting of the user's program has exceeded the preset overtime limit. At this time, please turn RUN/STOP switch to STOP and get program time-out address from special register D1008. (it is recommended to improve by WDT command)

ELC Input/Output Reaction Time

The total reaction time from the input signal to the output operation is calculated as follows: Reaction Time = input delay time + program scan time + output delay time

Input delay time	Factory setting is 10ms. Please refer to special registers D1020~1021.			
Program scan time	Please refer to the usage of special register D1010.			
Output delay time	Relay module: 10ms. Transistor module: 20~30us.			
Output delay time	Relay module: 10ms. Transistor module: 20~30us.			

Basic Commands and Application Commands of the ELC:

- The basic commands and the application commands of the ELC of this series are totally applicable to the ELC. Refer to the ELC Technique Application Manual for relevant basic commands and application commands.
- The ELC-HHP handheld programming panel and the ELCSoft (Windows version) editing program of the ladder diagram are both good for use with the ELC. Also, the ELC could connect with the ELC-PC12NNDR/T,ELC-PC12NNAR through specific transmission wire to execute the program transmission, the ELC control and the program monitoring.



#### TROUBLESHOOTING

Judge the errors by the indicators on the front panel. When errors occurred on ELC, please check:

#### 🌣 <u>"POWER" LED</u>

There is a "POWER" LED at the front of the ELC. When the ELC is powered On, the green LED light will be on.

Identify the status of the ELC. When the ELC is in operation, this light will be on, and users could thus use ELC-HHP or the editing program of the ladder diagram to give commands to make the ELC "RUN" or "STOP".

## If incorrect programs are input to the ELC, the indicator will blink. Please check D1004(Error code) and D1137(Error step number).

When the ERROR LED is on (not blinking), it indicates that the execution time of the program loop has exceeded the time-out setting (set by D1000). Please turn the ELC RUN/STOP switch to STOP, and find out the address of the time-out program by special data register D1008. "WDT". After completing modifying program, user only need to reload program and this LED will be off at this moment. Once the LED still lights, please turn off the power and check if there is interference or any conductive material in ELC.

#### ☆ <u>"BAT.LOW" LED</u>

When the battery voltage is low, the "BAT.LOW" LED will be on, please change the battery within 3 minutes to retain the ELC's internal user programs and data).

Input/Output" LED

The On/Off signals of the input point could be displayed through the "Input" LED. "Output" LED indicates if the output signals are On or Off.

\* "RS-232, RS-485" LED

"RS-232" LED will light when RS-232 is receiving data, "RS-485" LED will light when RS-485 is transferring data.